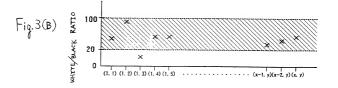


	(1, 1)	(2. 1)	(3. 1)	(4. 1)	(x-1, 1)	(x, 1)
F; q. 3 (A)	(1. 2)	(2, 2)	(3, 2)	(4. 2)	(x-1, 2)	(x, 2)
	(1, 3)	(2. 3)	(3, 3)	(4, 3)	(x-1, 3)	(x, 3)
	(1. 4)	(2, 4)	(3, 4)	(4, 4)	(x-1, 4)	(x. 4)
	(1, y-1)	(2. y-1)	(3. y-1)	(4, y-1)	(x-1, y-1)	(x, y-1)
	(1. y)	(2. y)	(3. y)	(4, y)	(x-1, y)	(x, y)



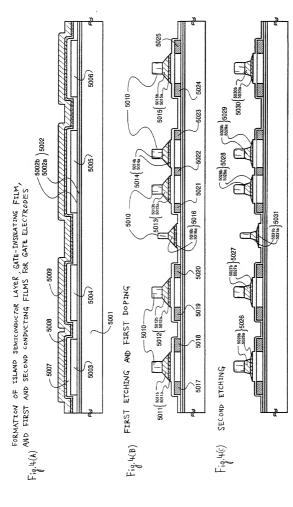
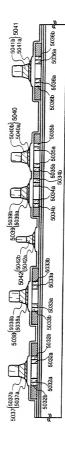


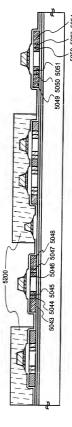




Fig. 5(B) THIRD ETCHING

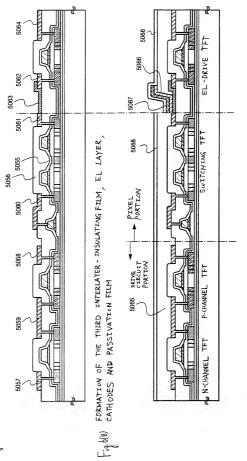


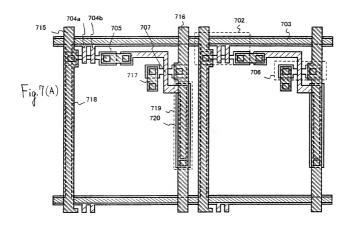
THIRD DOPING

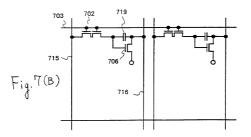


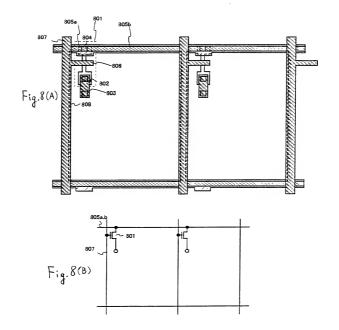
5052 5053 5054

FORMATION OF THE FIRST AND SECOND INTERLAYER-INSULATING FILMS Fig. 6(A) WIRINGS AND PIXEL ELECTRODES









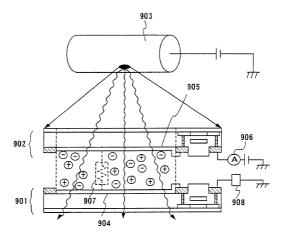
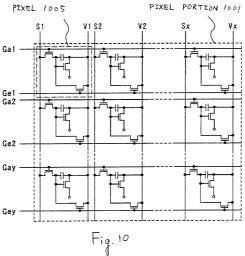
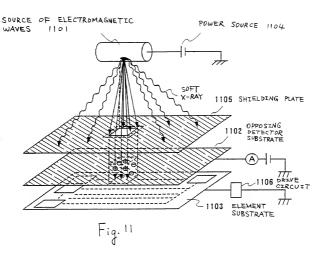
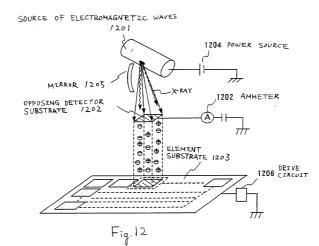
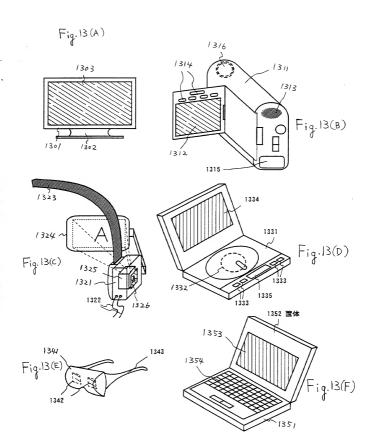


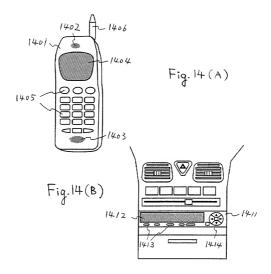
Fig. 9











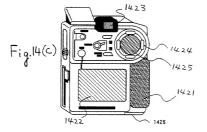
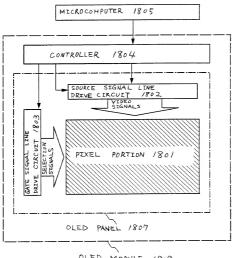


Fig. 15



OLED MODULE 1808

